Title:

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- Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
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March 12 2008

Date of Submission

Signature

Printed Name of Person Signing Certificate

Commissioner for Patents Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent.

Please enter these corrections. Since the errors appear to be on the part of the United States Patent Office, there should be no charge.

3 12 08 Date Respectfully submitted,

CHIU, JOANNA G. Attorney for Applicant(s) Registration No. 43629

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO.:

6859875 B1

APPLICATION NO:

09/592449

DATE:

06-12-2000

FIRST NAMED INVENTOR:

MOYER, WILLIAM C.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 9, Line 42, Claim No. 1:

Change "the central processing until comprise:." to--the central processing unit comprise:--

In Column 10, Line 33, Claim No. 8:

Change "the second mode or operation allows for a second address" to--the second mode of operation allows for a second address--

In Column 10, Line 40, Claim No. 9:

Change "address setup tinting is realized utilizing a first level" to--address setup timing is realized utilizing a first level--

In Column 10, Line 43, Claim No. 9:

Change "mote aggressive than the first level" to--more aggressive than the first level--

In Column 11, Line 2, Claim No. 14:

Change "node of operation," to--mode of operation,--

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.

Law Department

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